

BDMC Winter Webinar, 2020

Topic: NC-FinFET Scaling and NCFET Compact Model Enhancement

Abstract: The negative capacitance field-effect transistor (NCFET) is a promising technology for near future logic devices. The Webinar will start with the presentation of the scalability of NC-FinFETs by showing TCAD simulation results. The second part of the Webinar will demonstrate the enhancement of NCFET BDMC Compact Model. The enhancement includes implementation of domain interaction term, g , and improved convergence of the model. In short, the webinar will cover:

- Scalability of NC-FinFETs
- Compact Modeling of Domain Interaction Term
- Improved Convergence of the Model

Date: 19 March 2020

Time: 11 am – 12 noon Pacific time

06pm – 7 pm Pacific time



Details and updates are available at:

<http://www.bdmc.berkeley.edu/webinar/>

Speaker: Ming-Yen Kao



Ming-Yen Kao is currently pursuing the Ph.D. degree in electrical engineering with the University of California, Berkeley. Since 2017, he has been with BSIM Group, University of California and with Berkeley Device Modeling Center (BDMC). His current research interests include semiconductor devices physics, compact modeling, and TCAD simulation.

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